



# Information Disclosure Statement

USPN 10/763,071

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Form PTO-1449 (Modified)	ATTY DOCKET NO. B-4850 620354-1	U.S. SERIAL NO. 10/763,071
LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANTS Todd KAPLAN	
	FILING DATE January 21, 2004	GROUP not yet assigned

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUB- CLASS	FILING DATE or 102(e) DATE IF APPROPRIATE
<i>JPW</i>	4,083,043	4/1978	Breuer <i>341/165</i>	<i>340</i>	<i>341</i>	
	6,061,010	5/2000	Adams et al.	341	144	
	6,476,748 B1	11/2002	Galton	341	144	
	6,597,303 B2	7/2003	Cosand	341	165	
	6,621,438 B1	9/2003	Hong	341	144	
	6,628,220 B2	9/2003	Cosand	341	144	
<i>JPW</i>	10/761,790		Cosand	<i>✓</i>	<i>✓</i>	1/21/2004

## FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>JPW</i>	Adams, R., et al., "A 113-dB SNR Oversampling DAC With Segmented Noise-Shaped Scrambling," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 33, No. 12, pp. 1871-1878 (1998). <i>No merit</i>
	Adams, R., et al., "A 113dB SNR Oversampling DAC with Segmented Noise-Shaped Scrambling," <i>IEEE International Symposium on Circuits and Systems</i> , pp. 62-64 (1998). <i>No merit</i>
<i>JPW</i>	Chan, K.T., et al., "Components For A GAAS Delta-Sigma Modulator Oversampled Analog-To-Digital Converter," <i>IEEE International Symposium on Circuits and Systems</i> , pp. 1300-1303 (1992). <i>No merit</i>

EXAMINER <i>Patricia Wang</i>	DATE CONSIDERED <i>04/25/2005</i>
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.